

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	INTEGRATED CIRCUIT DESIGN FOR SIGNAL INTEGRITY, AVOIDING WELL PROXIMITY EFFECTS						
<p>Application Number : <i>10/708,715</i></p> <p>Confirmation Number:</p> <p>First Named Applicant: Karen Bard</p> <p>Attorney Docket Number: BUR920030172US1</p> <p>Art Unit: <i>2825</i></p> <p>Examiner: <i>LIN, SUN JAMES</i></p> <p>Search string: ( 5875117 or 20030145296 ).pn</p>							
<b>US Patent Documents</b>							
<p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p>							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
<i>QSL</i>	1	5875117	1999-02-23	Jones et al.	<i>A</i>	<i>716</i>	<i>14</i>
<b>US Published Applications</b>							
<p>Note: Applicant is not required to submit a paper copy of cited US Published Applications</p>							
init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
<i>QSL</i>	1	20030145296	2003-07-31	Chandra et al.	<i>A1</i>	<i>716</i>	<i>5</i>
<b>Signature</b>							
Examiner Name				Date			
<i>James Sun Lin</i>				<i>11-15-05</i>			